SIPMOS® Power-Transistor

Feature
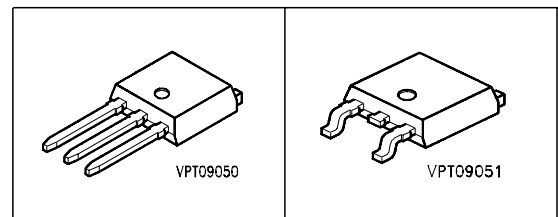
- N-Channel
- Enhancement mode
- 175°C operating temperature
- Avalanche rated
- dv/dt rated

Product Summary
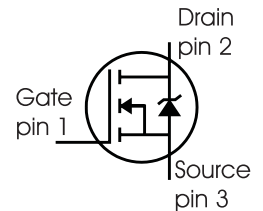
V_{DS}	100	V
$R_{DS(on)}$	179	mΩ
I_D	10.5	A

P-TO251   P-TO252

VPT09050   VPT09051

Drain pin 2

Gate pin 1

Source pin 3

Type	Package	Ordering Code	Marking
SPD11N10	P-TO252	-	11N10
SPU11N10	P-TO251	-	11N10

Maximum Ratings, at T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Continuous drain current	I_D		A
T_C=25°C		10.5	
T_C=100°C		-	
Pulsed drain current T_C=25°C	$I_{D\ puls}$	42	
Avalanche energy, single pulse I_D=10.5 A , V_{DD}=25V, R_{GS}=25Ω	E_{AS}	60	mJ
Reverse diode dv/dt I_S=10.5A, V_{DS}=80V, di/dt=200A/µs, T_{jmax}=175°C	dv/dt	6	kV/µs
Gate source voltage	V_{GS}	±20	V
Power dissipation T_C=25°C	P_{tot}	48	W
Operating and storage temperature	T_j , T_{stg}	-55... +175	°C
IEC climatic category; DIN IEC 68-1		55/175/56	

Thermal Characteristics

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Characteristics					
Thermal resistance, junction - case	R_{thJC}	-	-	3.1	K/W
Thermal resistance, junction - ambient, leaded	R_{thJA}	-	-	100	
SMD version, device on PCB:	R_{thJA}				
@ min. footprint		-	-	75	
@ 6 cm^2 cooling area [1]		-	-	50	

Electrical Characteristics, at T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Drain-source breakdown voltage V_{GS}=0V, I_D=1mA	$V_{(BR)DSS}$	100	-	-	V
Gate threshold voltage, V_{GS} = V_{DS} I_D = 21 µA	$V_{GS(th)}$	2.1	3	4	
Zero gate voltage drain current V_{DS}=100V, V_{GS}=0V, T_j=25°C V_{DS}=100V, V_{GS}=0V, T_j=125°C	I_{DSS}	- -	0.01 1	1 100	µA
Gate-source leakage current V_{GS}=20V, V_{DS}=0V	I_{GSS}	-	1	100	nA
Drain-source on-state resistance V_{GS}=10V, I_D=-A	$R_{DS(on)}$	-	tbd	179	mΩ

[1]Device on 40mm*40mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70 µm thick) copper area for drain connection. PCB is vertical without blown air.

Electrical Characteristics, at T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Dynamic Characteristics						
Transconductance	g_{fs}	$V_{DS} \geq 2*I_D*R_{DS(on)max}$, I_D=-A	tbd	tbd	-	S
Input capacitance	C_{iss}	V_{GS}=0V, V_{DS}=25V, f=1MHz	-	tbd	tbd	pF
Output capacitance	C_{oss}		-	tbd	tbd	
Reverse transfer capacitance	C_{rss}		-	tbd	tbd	
Turn-on delay time	$t_{d(on)}$	V_{DD}=50V, V_{GS}=10V, I_D=10.5A, R_G=27Ω	-	tbd	tbd	ns
Rise time	t_r		-	tbd	tbd	
Turn-off delay time	$t_{d(off)}$		-	tbd	tbd	
Fall time	t_f		-	tbd	tbd	
Gate Charge Characteristics						
Gate to source charge	Q_{gs}	V_{DD}=80V, I_D=10.5A	-	tbd	tbd	nC
Gate to drain charge	Q_{gd}		-	tbd	tbd	
Gate charge total	Q_g	V_{DD}=80V, I_D=10.5A, V_{GS}=0 to 10V	-	tbd	tbd	
Gate plateau voltage	$V_{(plateau)}$	V_{DD}=80V, I_D=10.5A	-	tbd	-	V
Reverse Diode						
Inverse diode continuous forward current	I_S	T_C=25°C	-	-	10.5	A
Inverse diode direct current, pulsed	I_{SM}		-	-	42	
Inverse diode forward voltage	V_{SD}	V_{GS}=0V, I_F=10.5A	-	tbd	tbd	V
Reverse recovery time	t_{rr}	V_R=50V, I_F=I_S, di_F/dt=100A/µs	-	tbd	tbd	ns
Reverse recovery charge	Q_{rr}		-	tbd	tbd	nC

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